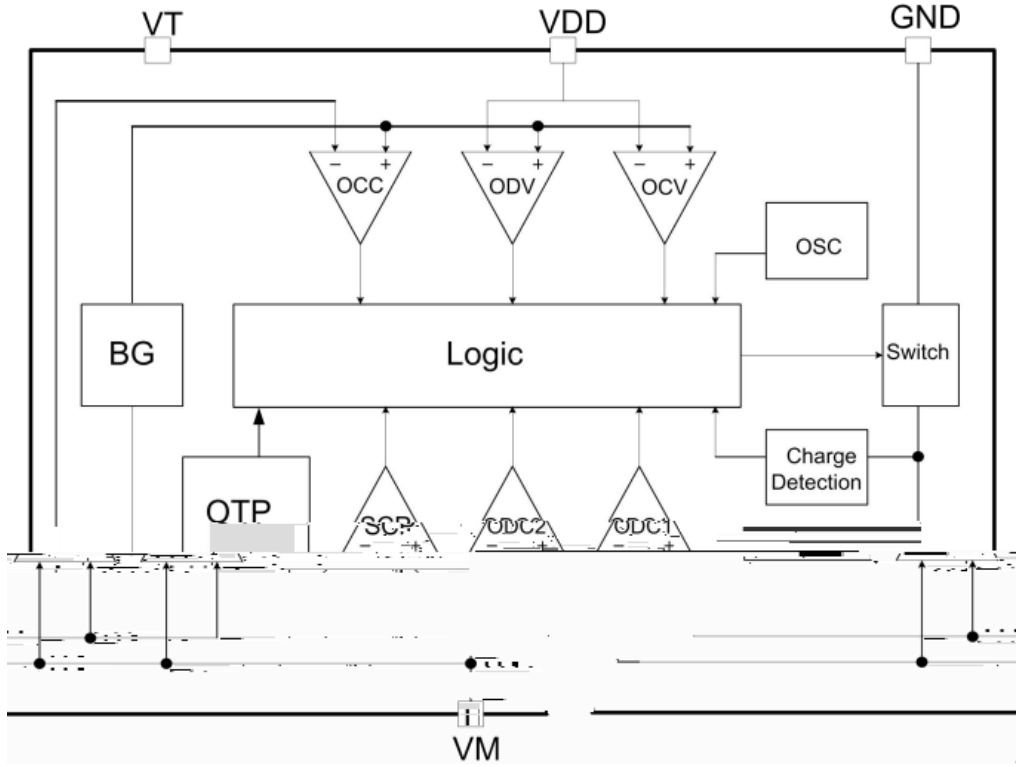


Functionl Block Diagram



Functional Description

The device is a high-precision, low-power, single-channel, differential input, single-ended output, auto-zeroing, sigma-delta ADC. It is designed for applications requiring high accuracy and low power consumption. The device features a built-in oscillator, a programmable gain amplifier (PGA), and a digital-to-analog converter (DAC). The device is available in a variety of packages and is compatible with a wide range of microcontrollers.

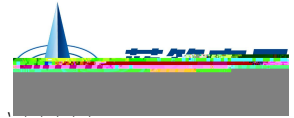
Normal Operating mode

In normal operating mode, the device is configured to operate as a single-channel, differential input, single-ended output, auto-zeroing, sigma-delta ADC. The device is designed to provide high accuracy and low power consumption. The device is compatible with a wide range of microcontrollers.

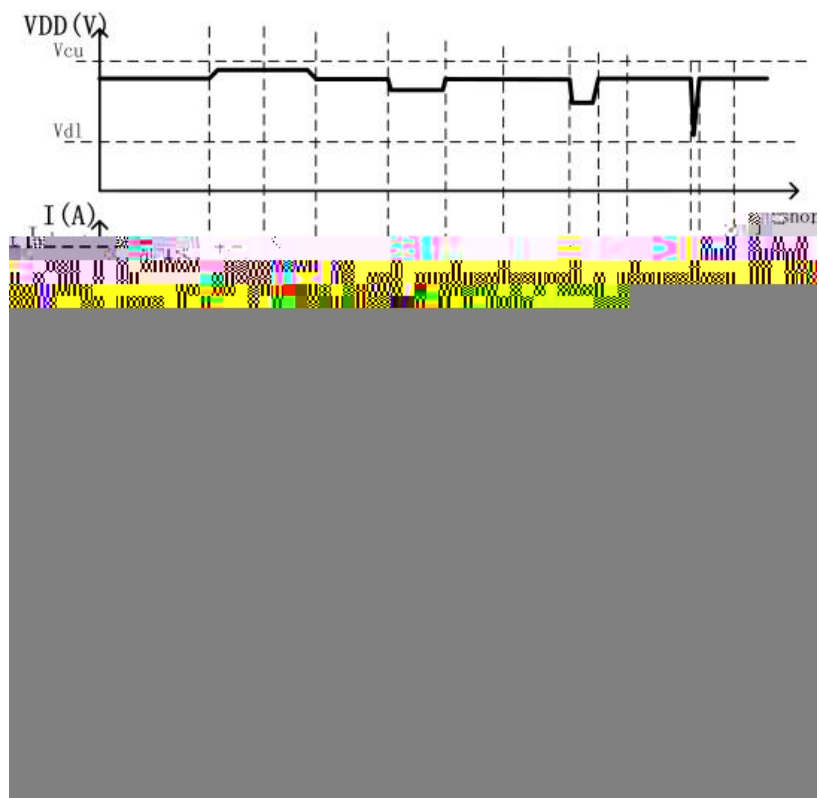
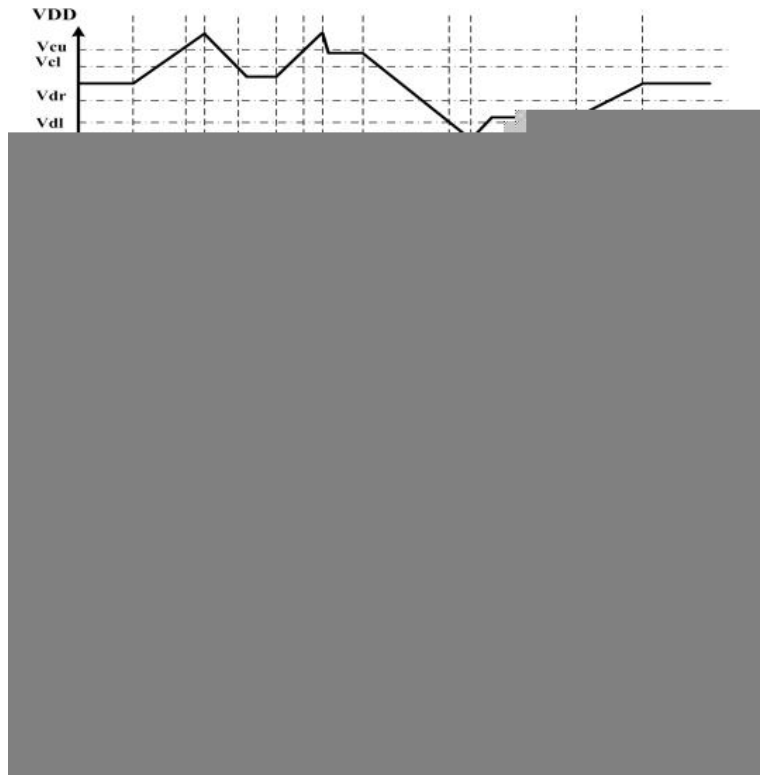


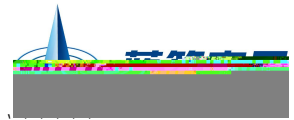




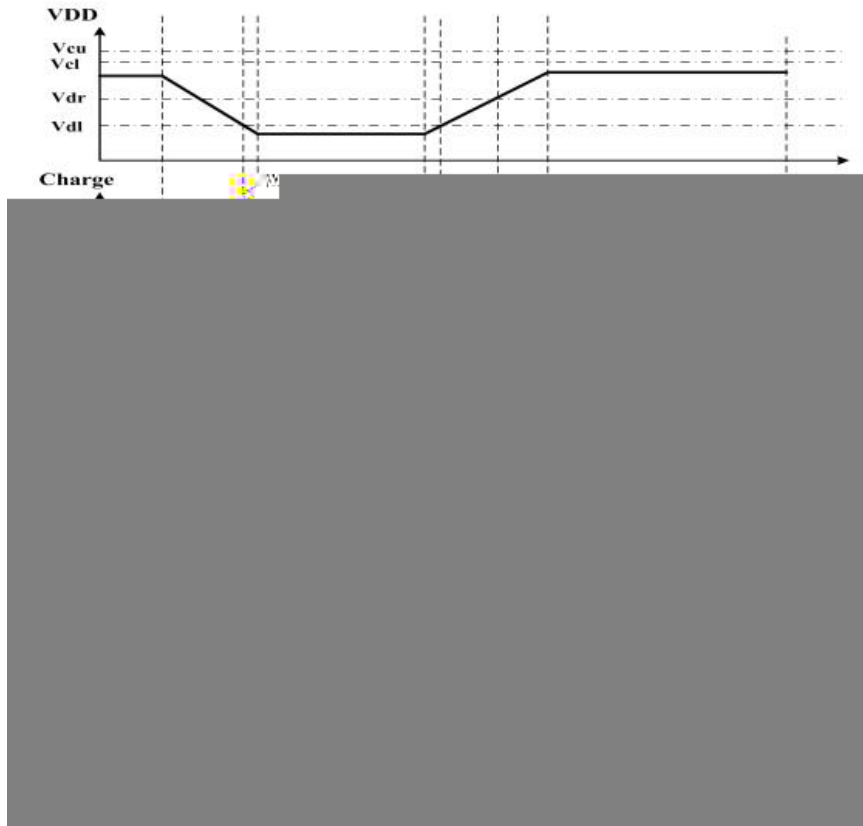


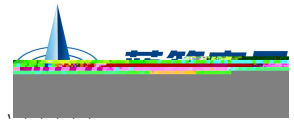
/ Timing Chart





/ Timing Chart





/ Package Dimensions

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DFN2 ×2-6L-0.5

Unit:mm

