

出脚	定义
Pin1	S
Pin2	S
Pin3	S
Pin4	G
Pin5	D
Pin6	D
Pin7	D
Pin8	D





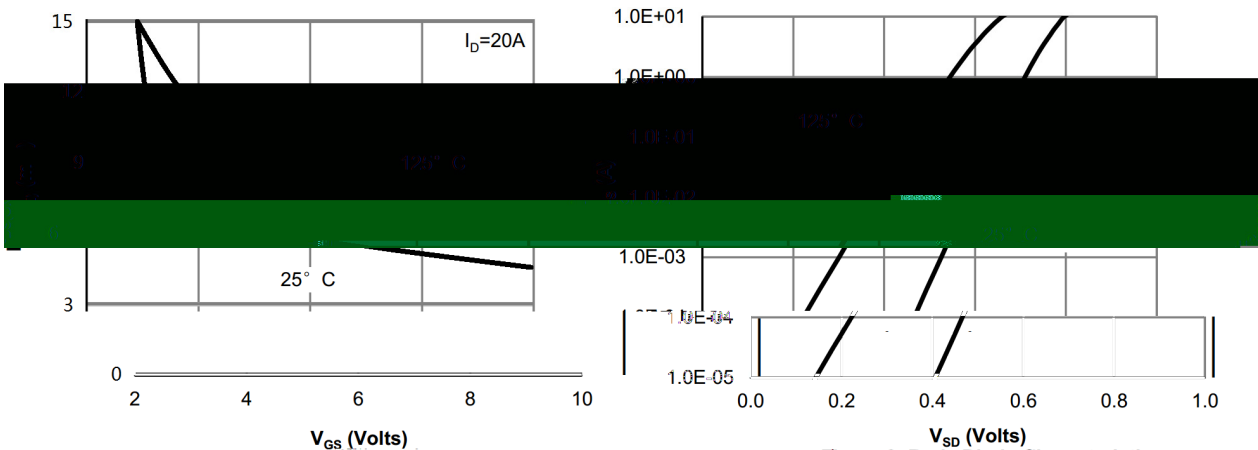
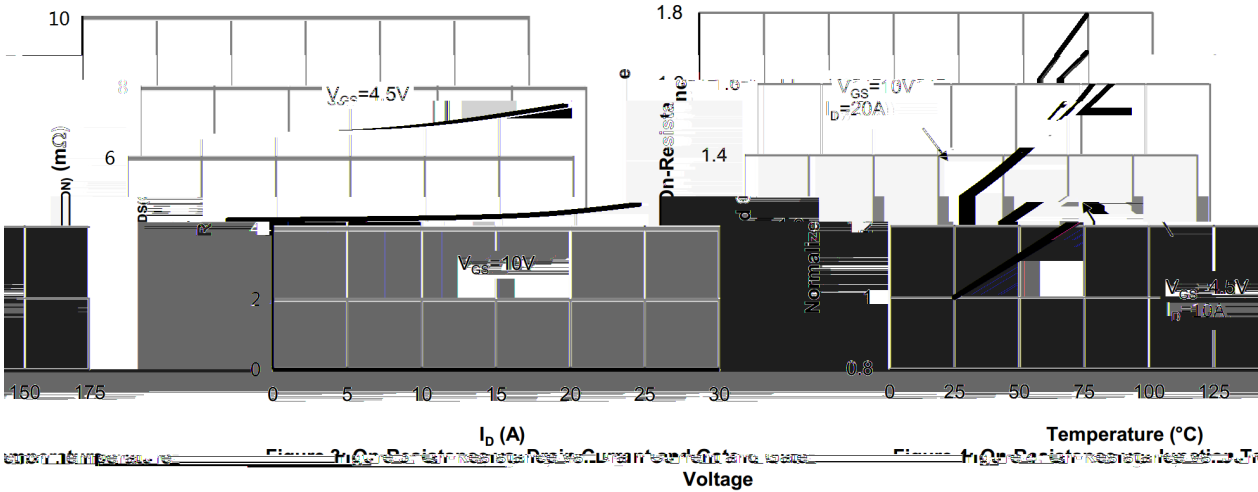
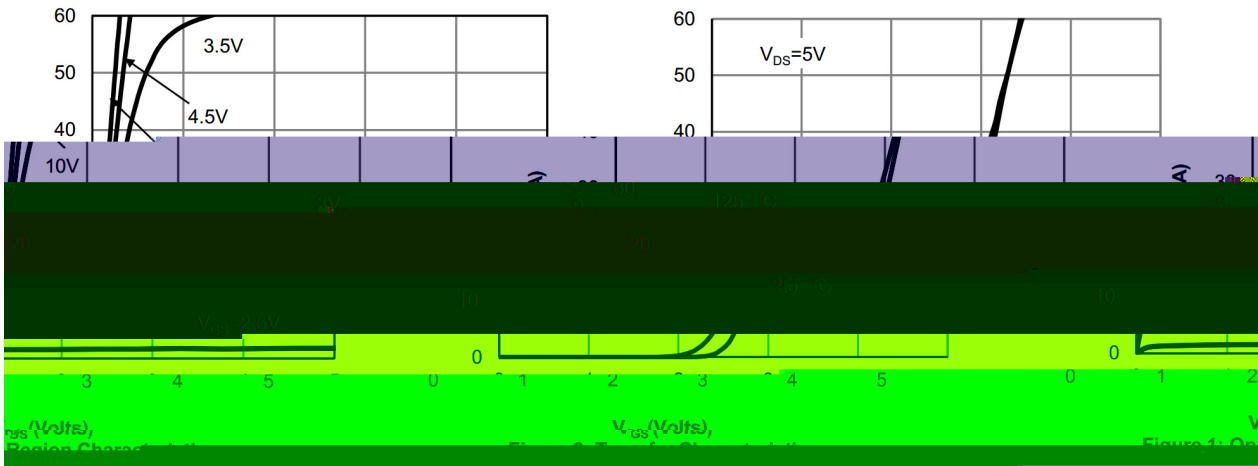


Figure 5: On-Resistance vs. Gate-Source Voltage

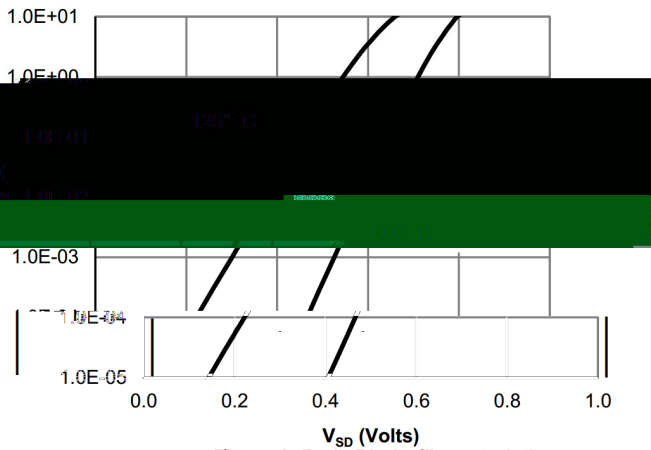


Figure 6: Body-Diode Characteristics

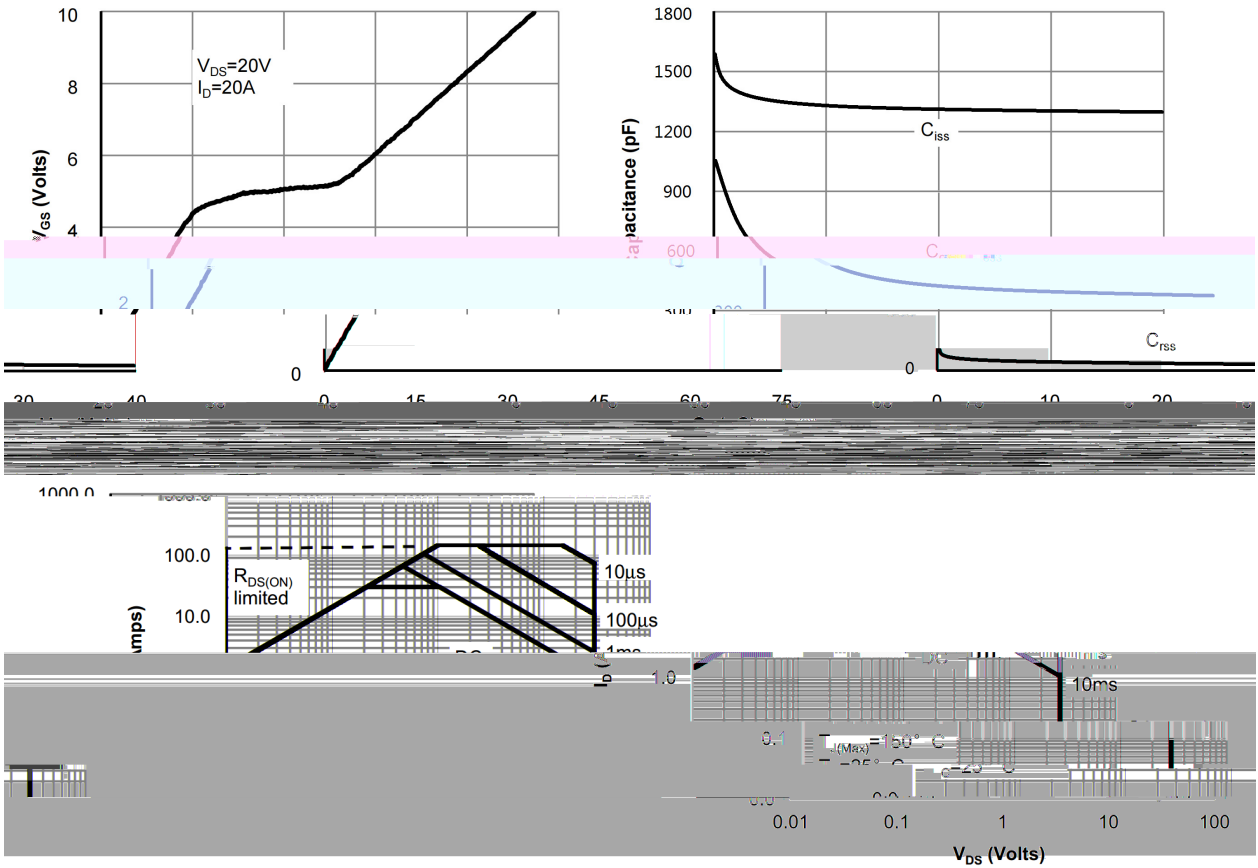


Figure 9: Maximum Power Dissipation Operating Area

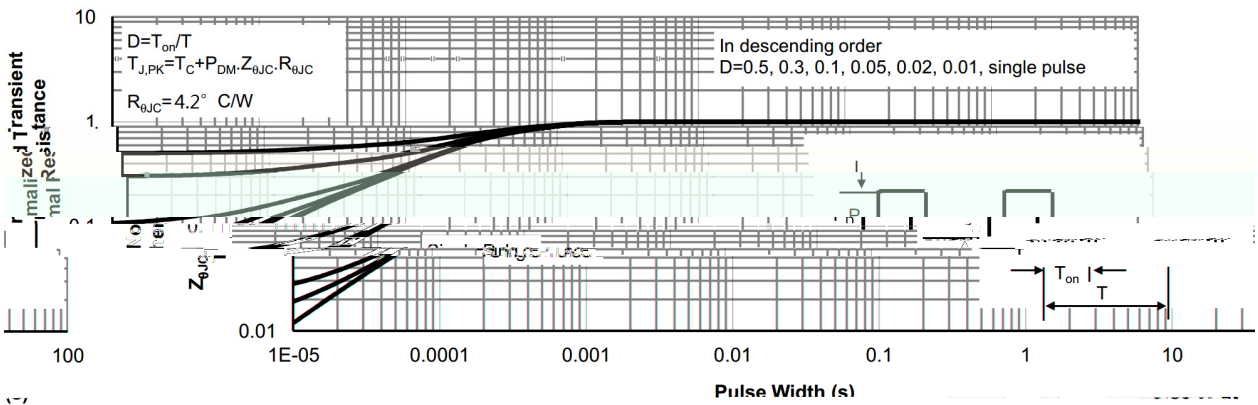
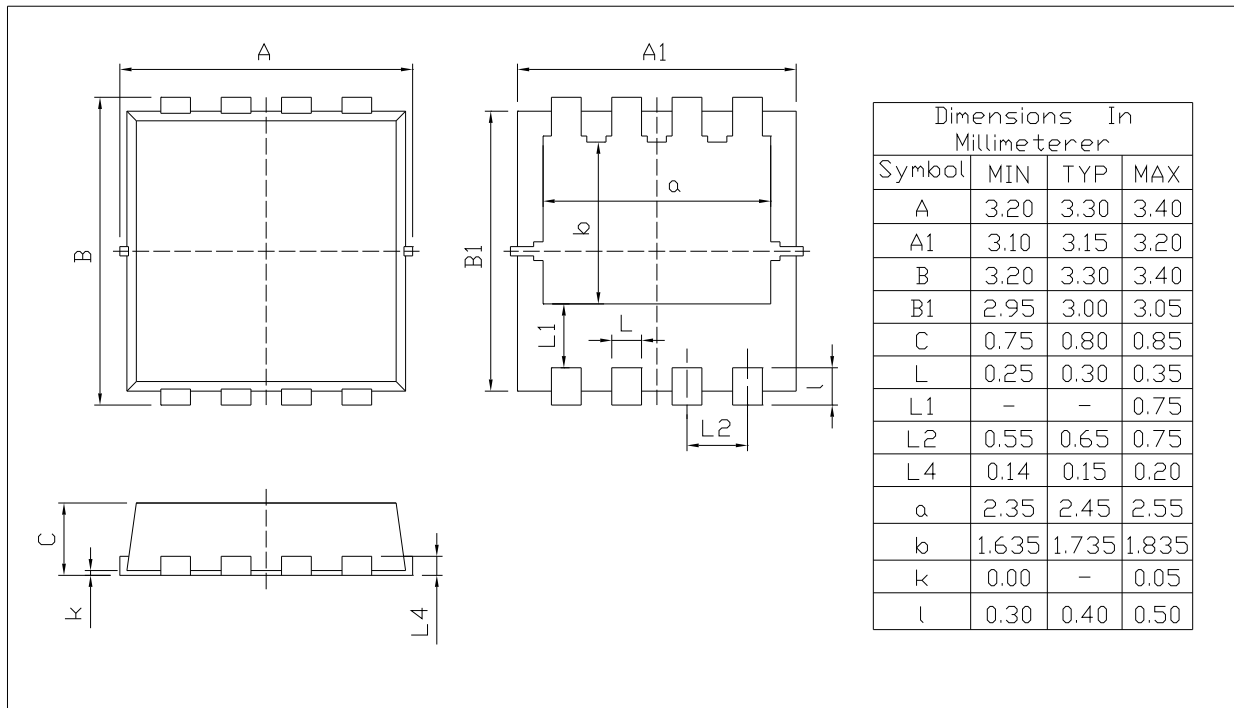


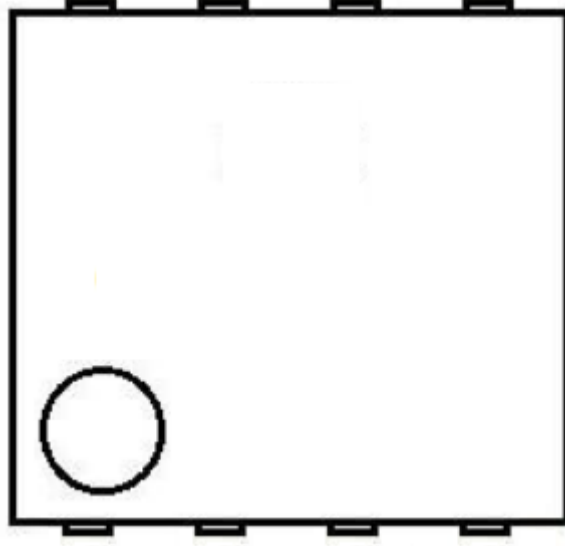
Figure 10: Normalized Maximum Trans

PDFN3X3A-8L

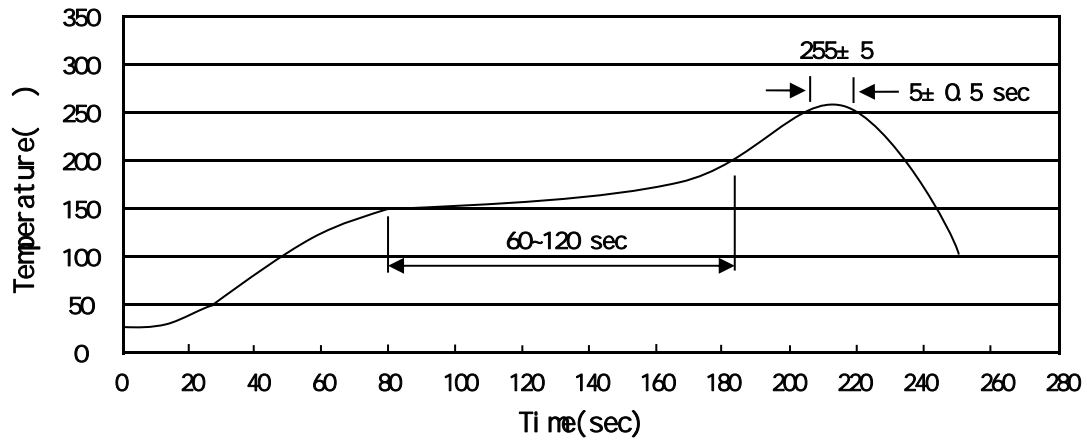
Unit:mm



Rev.00 202011



### Temperature Profile for IR Reflow Soldering(Pb-Free)



	Units/Reel	Reels/Inner Box	Units/Inner Box	Inner Boxes/Outer Box	Units/Outer Box	Reel	Inner Box	Outer Box
	/	/	/	/	/			