

**/ Descriptions**

PDFN 3×3A-8L          N          MOS

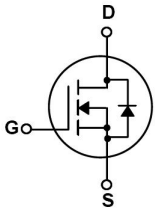
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**/ Features**

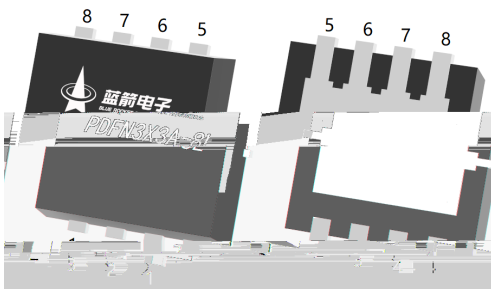
**/ Applications**

DC/DC

**/ Equivalent Circuit**



**/ Pinning**



出脚	定义
Pin1	S
Pin2	S
Pin3	S
Pin4	G
Pin5	D
Pin6	D
Pin7	D
Pin8	D

**/ Marking**



		$\mu$				
						$\mu$
						$\mu$
		$\mu$				
						$\Omega$
						$\Omega$
						$\Omega$

/ Electrical Characteristic Curve

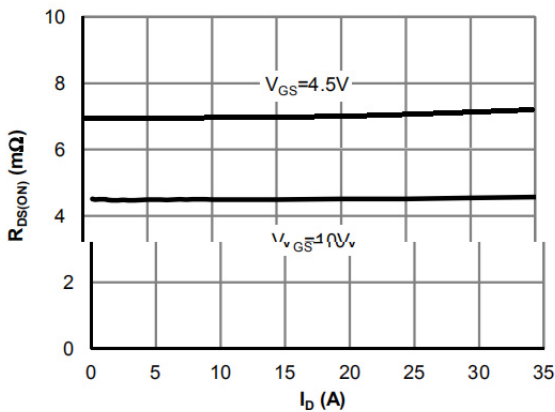
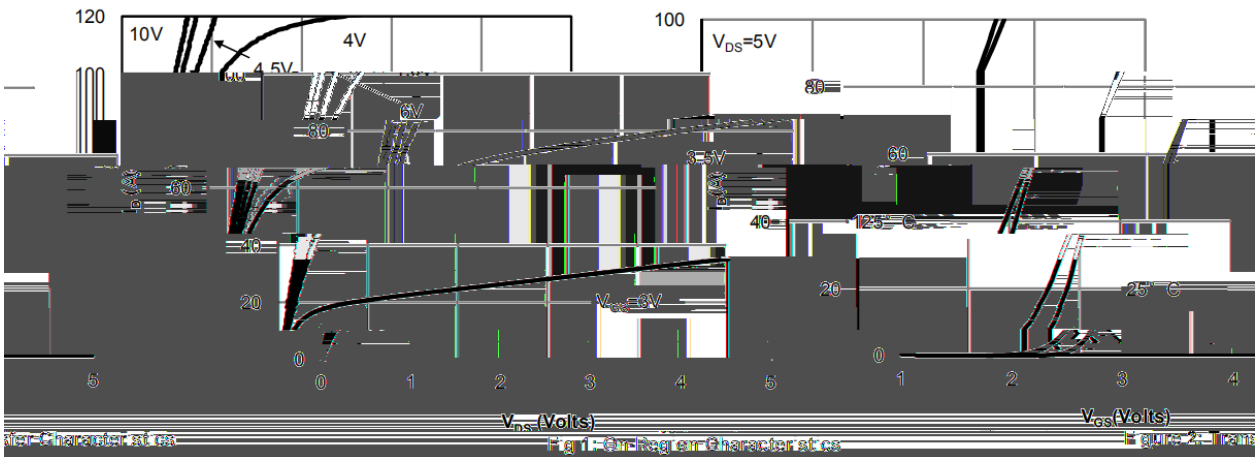


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

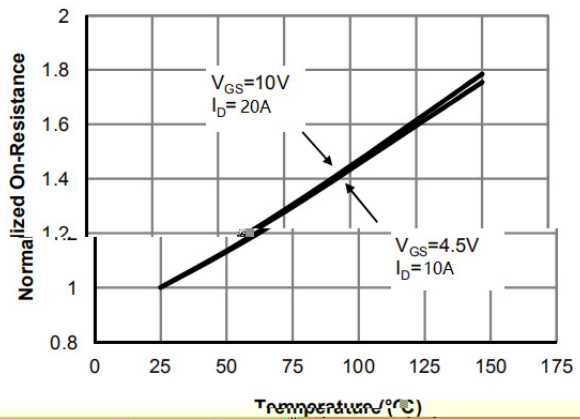


Figure 4: On-Resistance vs. Junction Temperature

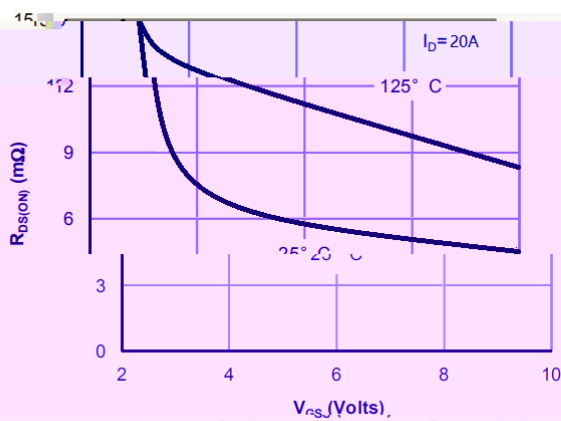


Figure 5: On-Resistance vs. Gate-Source Voltage

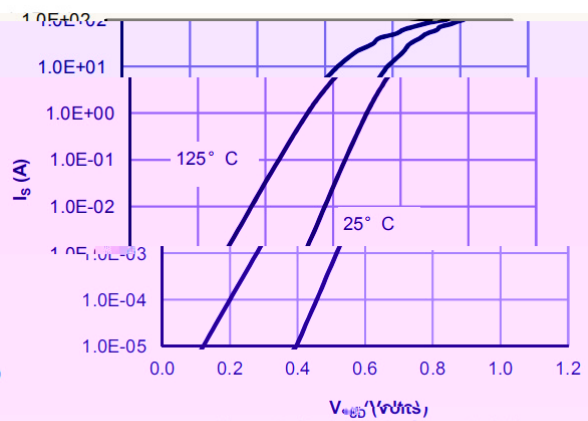


Figure 6: Drain Current Characteristic

/ Electrical Characteristic Curve

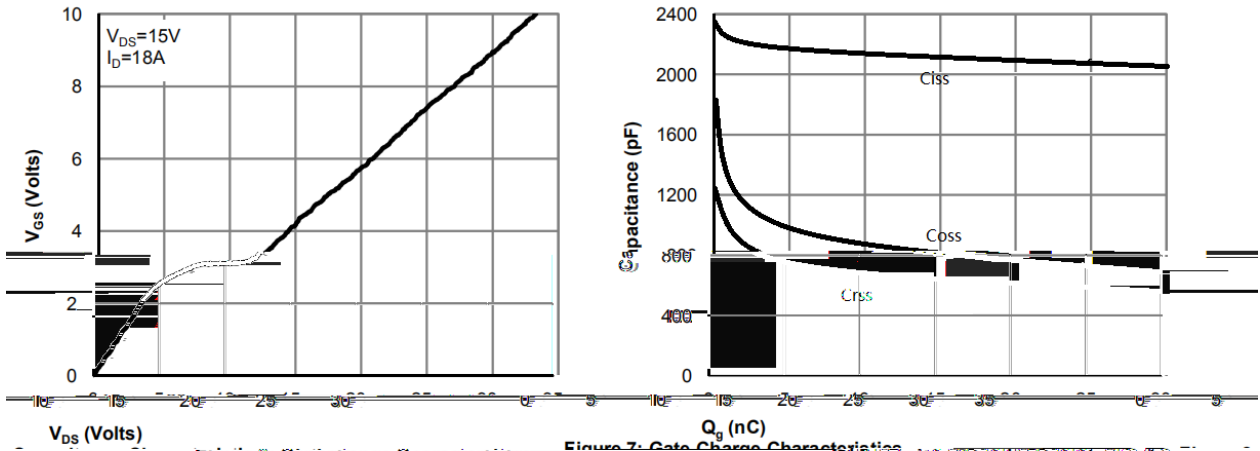


Figure 7: Gate Charge Characteristics

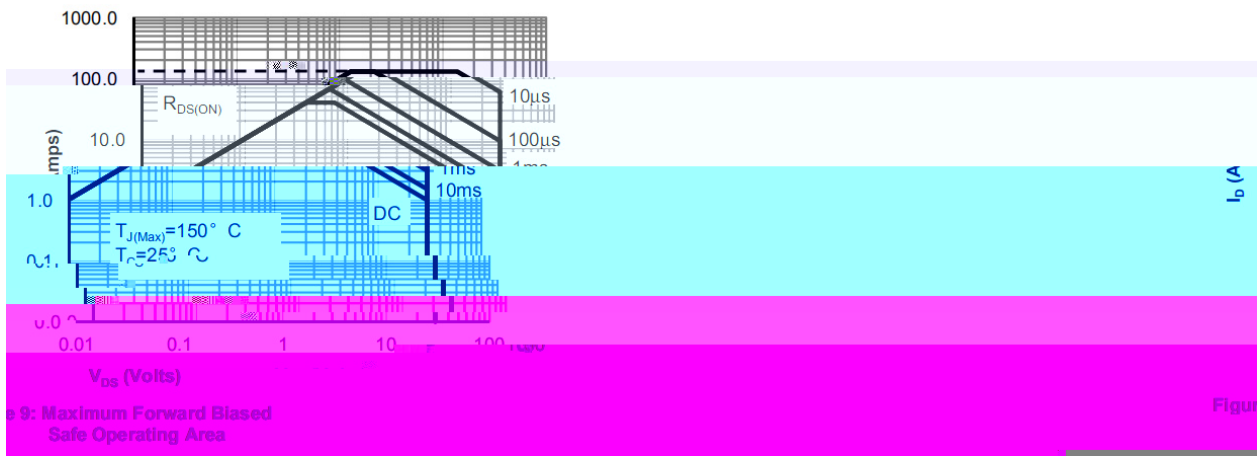


Figure 9: Maximum Forward Biased Safe Operating Area

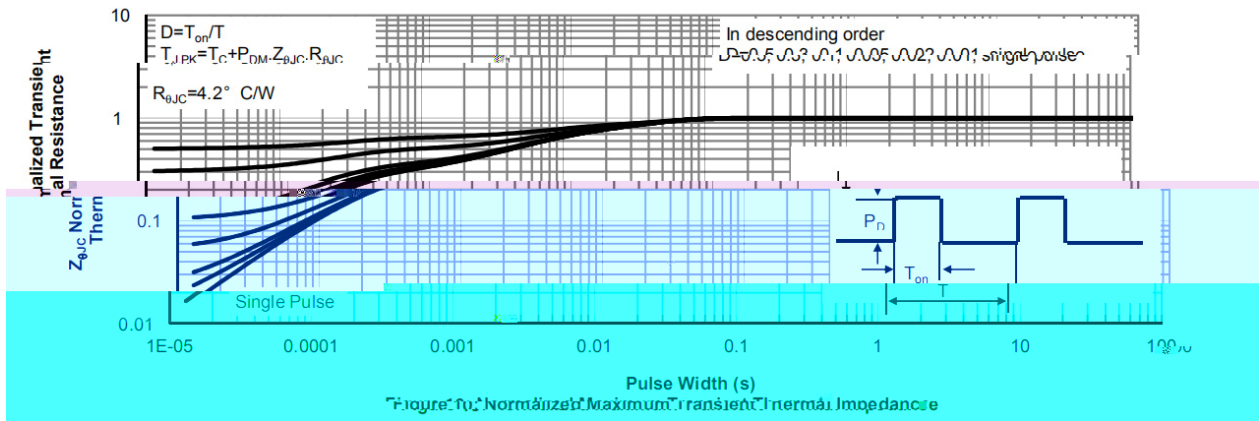
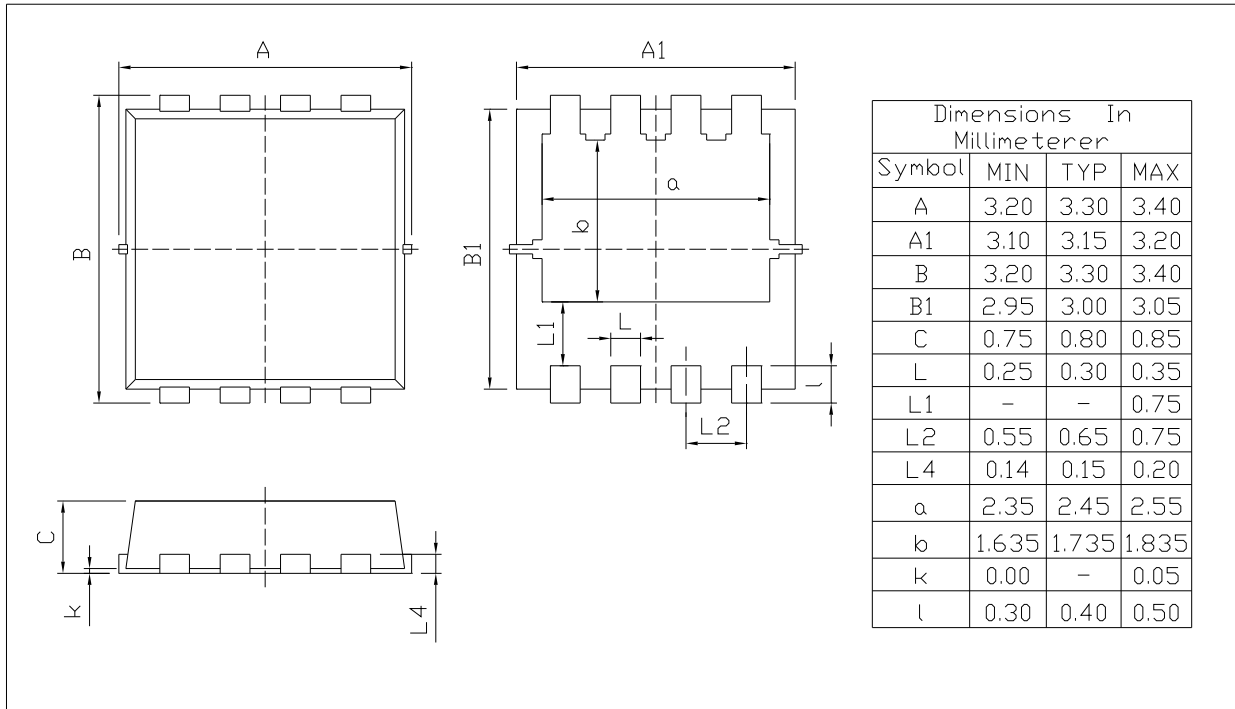


Figure 10: Normalized Maximum Thermal Impedance

**/ Package Dimensions**

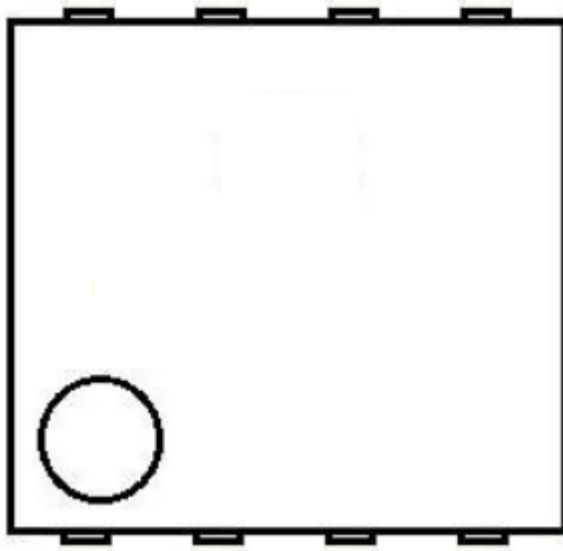
PDFN3X3A-8L

Unit:mm



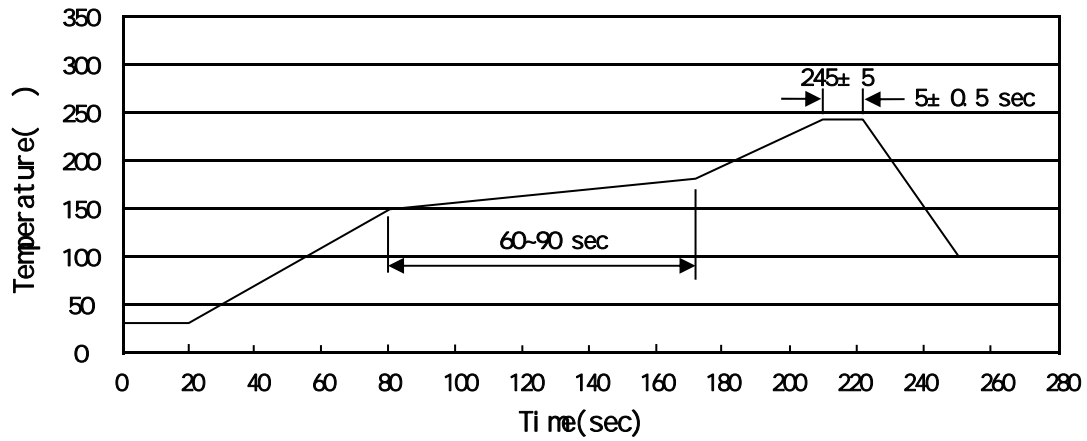
Rev.00 202011

/ Marking Instructions



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( ) / Temperature Profile for IR Reflow Soldering(Pb-Free)



± ± ± ±

/ Resistance to Soldering Heat Test Conditions

± ±

/ Packaging SPEC.

	Units/Reel	Reels/Inner Box	Units/Inner Box	Inner Boxes/Outer Box	Units/Outer Box	Reel	Inner Box	Outer Box
	/	/	/	/	/			

/ Notices