

Rev.A Aug.-2025

PDFN 3×3A-8L P MOS

P-Channel Enhancement Mode Field Effect Transistor in a PDFN3×3A-8L Plastic Package.

$V_{DS} (V) = -16V$ $I_D = -49.5A$ ($V_{GS} = \pm 12V$)

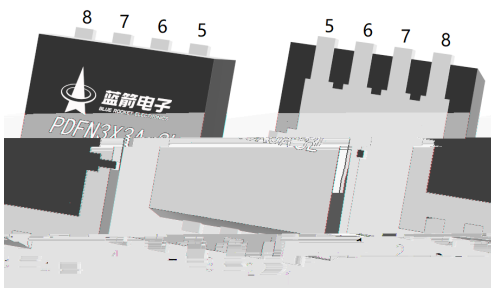
$R_{DS(ON)}@ -4.5V$ 8m (Typ. 6.6m)

$R_{DS(ON)}@ -2.5V$ 15m (Typ. 9.8m)

HF Product.

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Notebook AC-in load switch, Battery protection charge/discharge.

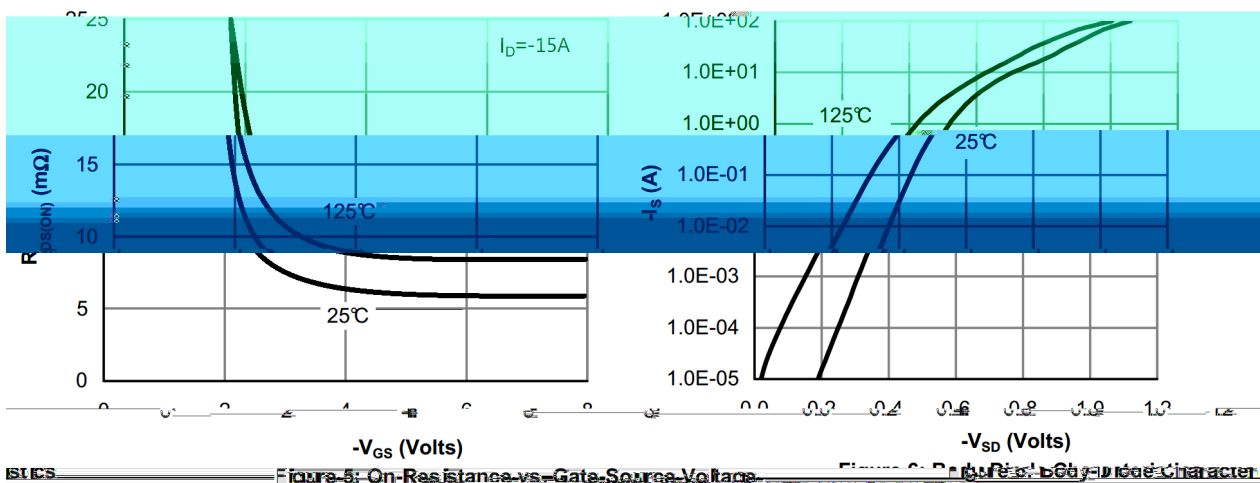
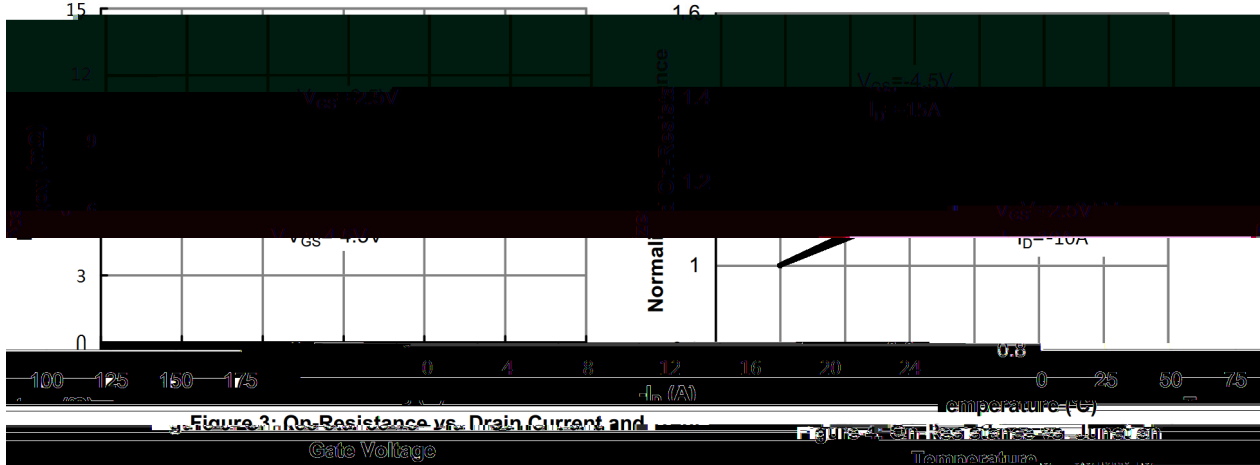
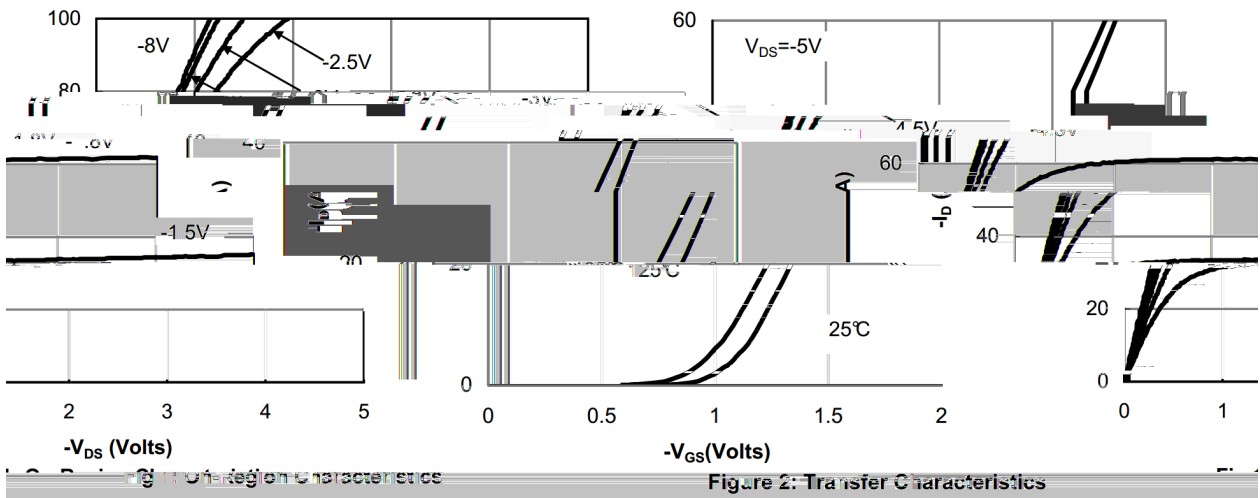


See Marking Instructions.

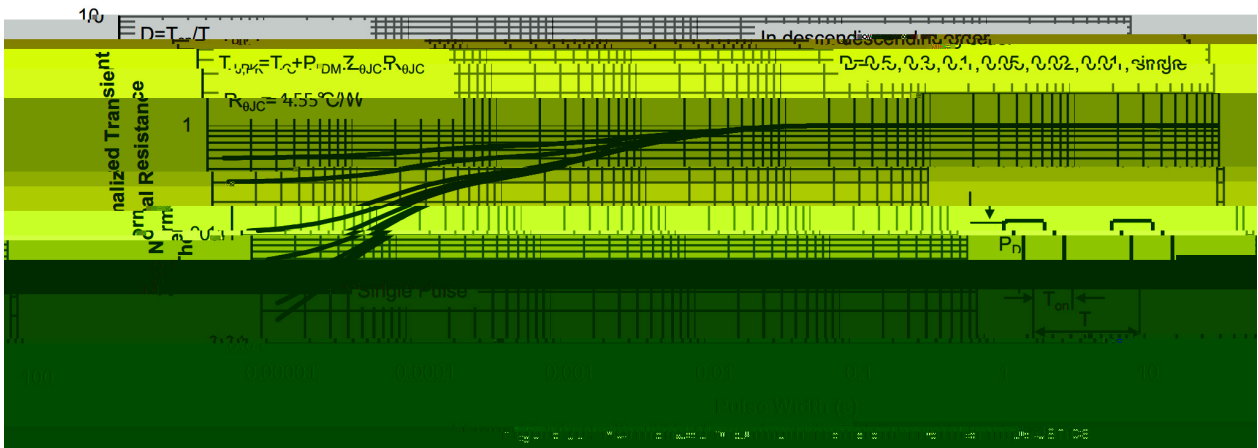
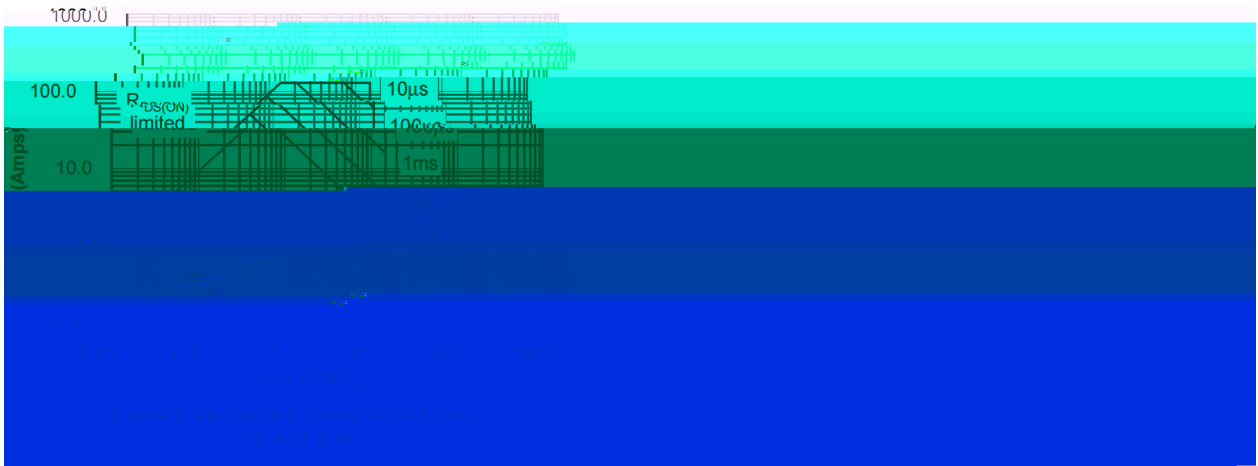
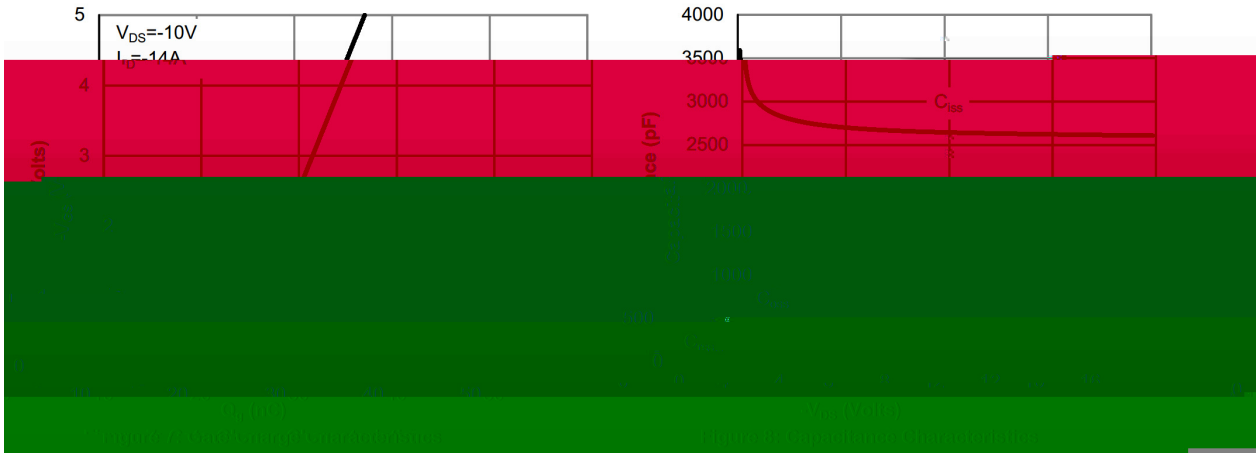
Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	-16	V
Drain Current - Continuous	$I_D(T_c=25^\circ\text{C})$	-49.5	A
Drain Current - Continuous	$I_D(T_c=100^\circ\text{C})$	-33	A
Drain Current – Pulsed	I_{DM}	-132	A
Gate-Source Voltage	V_{GS}	± 12	V
Power Dissipation	$P_D(T_c=25^\circ\text{C})$	27.5	W
Power Dissipation	$P_D(T_c=100^\circ\text{C})$	11	W
Single Pulse Avalanche Energy(L=0.5mH)	E_{AS}	160	mJ
Avalanche Current	I_{AS}	-15.5	A
Junction and Storage Temperature Range	T_j, T_{stg}	-55 to 150	
Thermal resistance, junction - ambient	t 10s	R_{JA}	/ W
	Steady-State		
Thermal resistance, junction - case	Steady-State	R_{JC}	4.55

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D=-250\mu\text{A}$ $V_{GS}=0\text{V}$	-16	-18.5		V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-12.8\text{V}$ $V_{GS}=0\text{V}$			-1	μA
Gate-Body leakage current	I_{GSS}	$V_{DS}=0\text{V}$, $V_{GS}=\pm 12\text{V}$			± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$ $I_D=-250\mu\text{A}$	-0.5	-0.76	-1.0	V
Static Drain-Source On-Resistance	$R_{DS(ON)}$	$V_{GS}=-4.5\text{V}$, $I_D=-15\text{A}$		6.6	8	m
		$V_{GS}=-2.5\text{V}$, $I_D=-10\text{A}$		9.8	15	
		$V_{GS}=-1.8\text{V}$, $I_D=-5\text{A}$		18		
Diode Forward Voltage	V_{SD}	$I_S=-1\text{A}$, $V_{GS}=0\text{V}$			-1.2	V
Reverse Recovery Time	t_{rr}	$V_{DS}=-10\text{V}$, $I_D=-14\text{A}$		2.9		ns
Reverse Recovery Charge	Q_{rr}	$dI_{SD}/dt = 100 \text{ A}/\mu\text{s}$		8		nC
Input Capacitance	C_{iss}	$V_{DS}=-15\text{V}$ $V_{GS}=0\text{V}$ $f=1.0\text{MHz}$		2620		pF
Output Capacitance	C_{oss}			440		
Reverse Transfer Capacitance	C_{rss}			375		
Gate resistance	R_g	$V_{GS}=0\text{V}$ $V_{DS}=0\text{V}$ $f=1\text{MHz}$		7.8		

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Total Gate Charge	Q_g	$V_{GS}=-4.5V, \quad V_{DS}=-10V,$ $I_D=-14A$		26.5		nC
Gate Source Charge	Q_{gs}			2.4		
Gate Drain Charge	Q_{gd}			9.7		
Turn-On Delay Time	$t_{d(on)}$	$V_{GS}=-4.5V \quad V_{DS}=-10V$ $I_D=-14A \quad R_G=3$		15.7		ns
Turn-On Rise Time	t_r			116		
Turn-Off Delay Time	$t_{d(off)}$			86.1		
Turn-Off Fall Time	t_f			121		

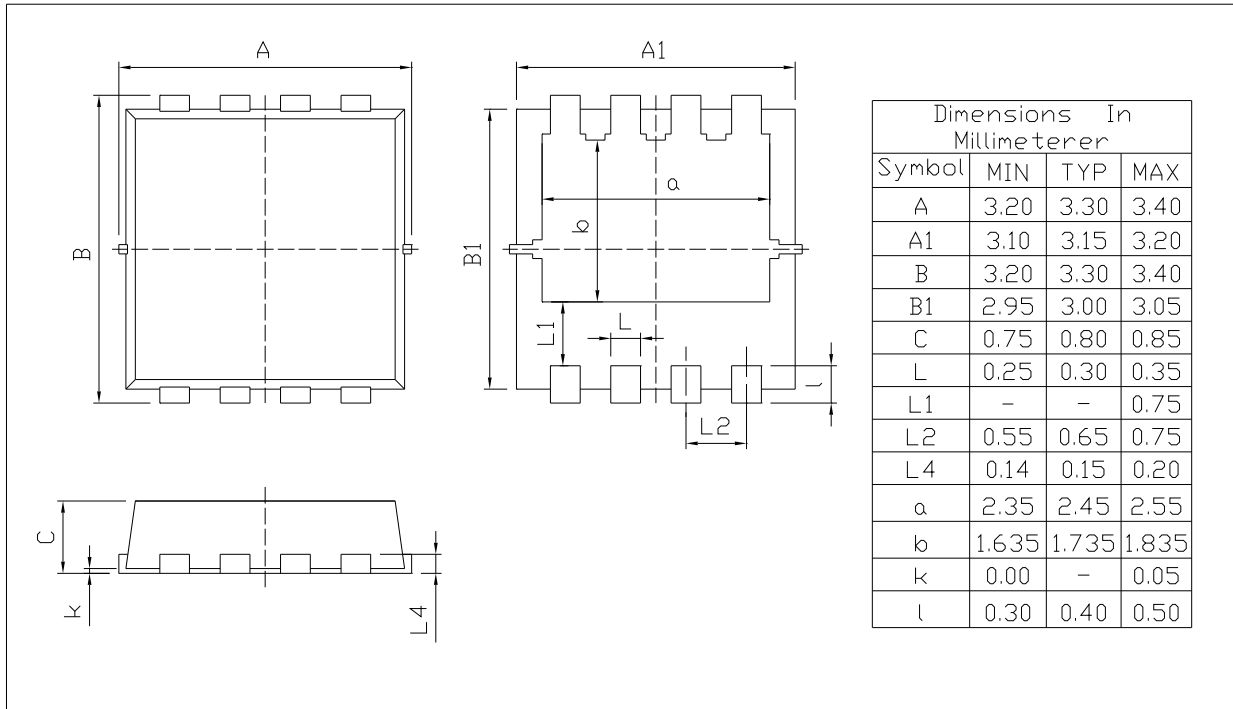


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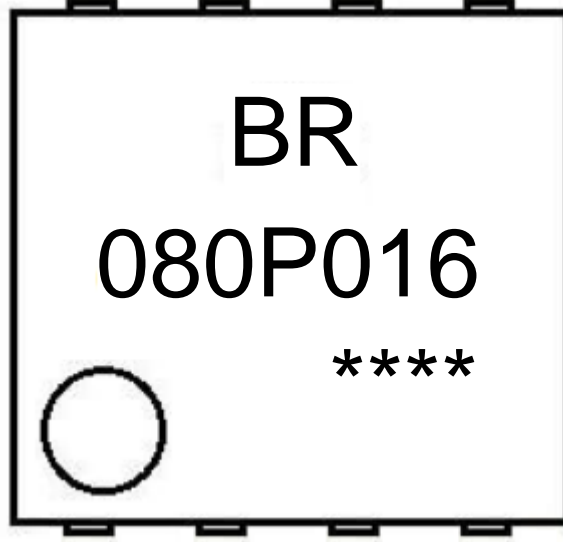


PDFN3X3A-8L

Unit:mm



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BR

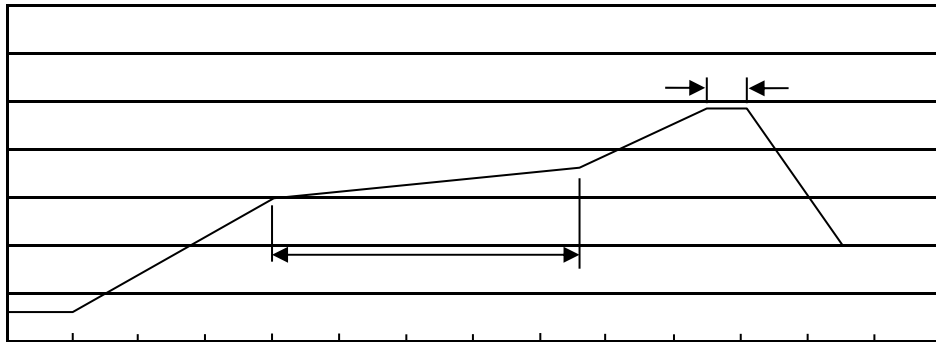
080P016

Note:

BR: Company Code

080P016: Product Type Code

****: Lot No. Code, code change with Lot No.

Temperature Profile for IR Reflow Soldering(Pb-Free)

Note:

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|---|---------|-----------|---|
| 1 | 150 180 | 60 90sec; | 1.Preheating:150~180 , Time:60~90sec. |
| 2 | 245±5 | 5±0.5sec; | 2.Peak Temp.:245±5 , Duration:5±0.5sec. |
| 3 | 2 10 | /sec. | 3. Cooling Speed: 2~10 /sec. |

260±5

10±1 sec.

Temp.:260±5

Time:10±1 sec

/ REEL

Package Type	Units					Dimension (unit mm ³)		
	Units/Reel	Reels/Inner Box	Units/Inner Box	Inner Boxes/Outer Box	Units/Outer Box	Reel	Inner Box	Outer Box
PDFN3x3A-8L	5,000	2	10,000	6	60,000	13 x12	360x360x50	380x335x366

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