

**/ Descriptions**

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P-Channel Enhancement Mode Field Effect Transistor in a DFN1006-3L Plastic Package.

**/ Features**

$V_{DS}=-20V$   $I_D=-0.7A$   $V_{GS}=\pm 12V$

$R_{DS(on)}@-4.5V$  520m (Type.390m )

$R_{DS(on)}@-2.5V$  780m (Type.530m )

$R_{DS(on)}@-1.8V$  1100m (Type.700m )

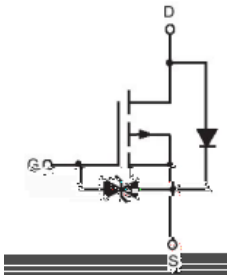
ESD-HBM 2000V

HF Product.

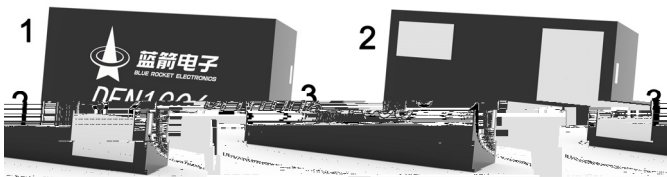
**/ Applications**

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**/ Equivalent Circuit**



**/ Pinning**



Pin1:G

Pin2:S

Pin3:D

**/ Marking**

See Marking Instructions.

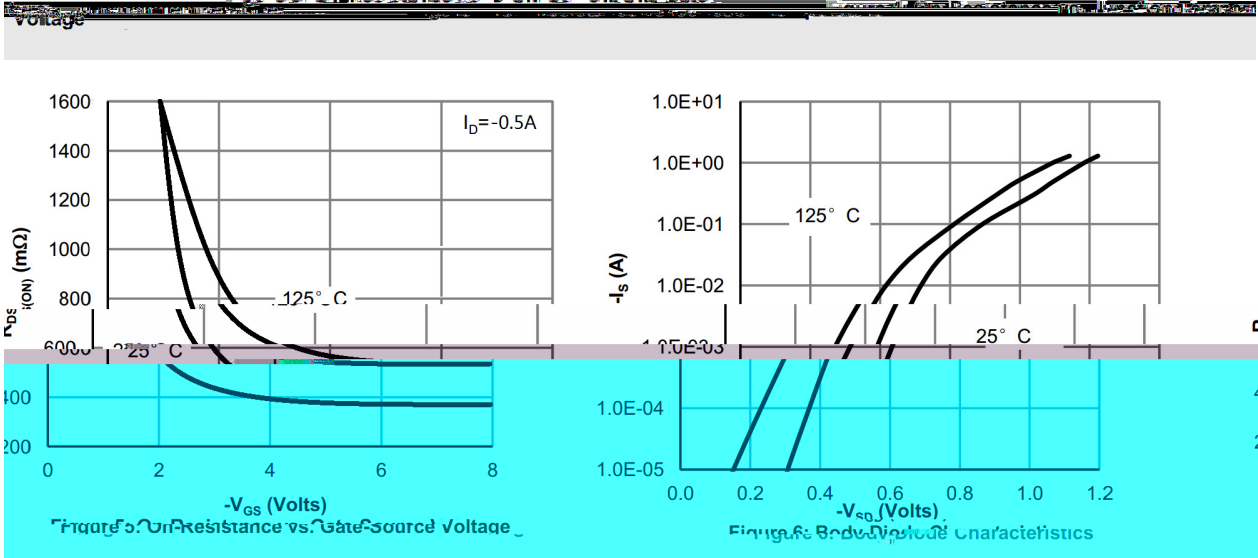
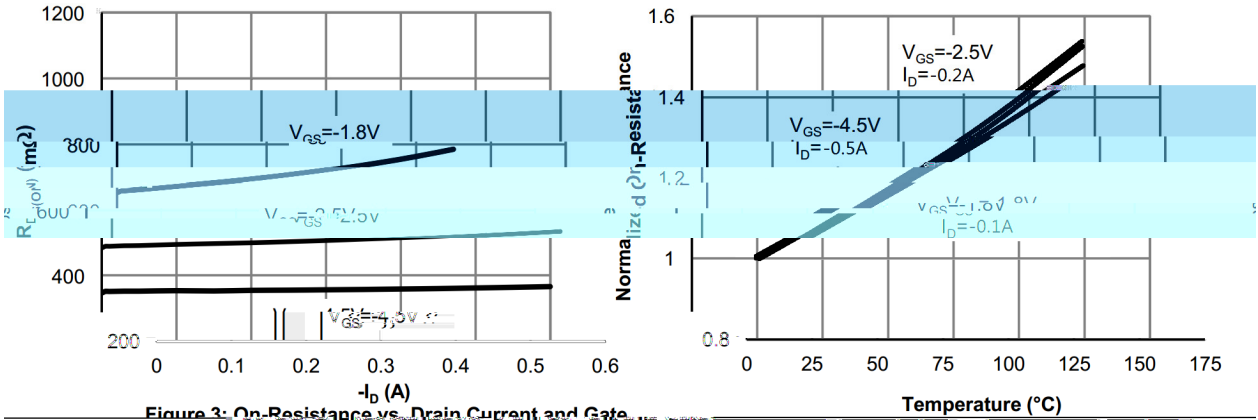
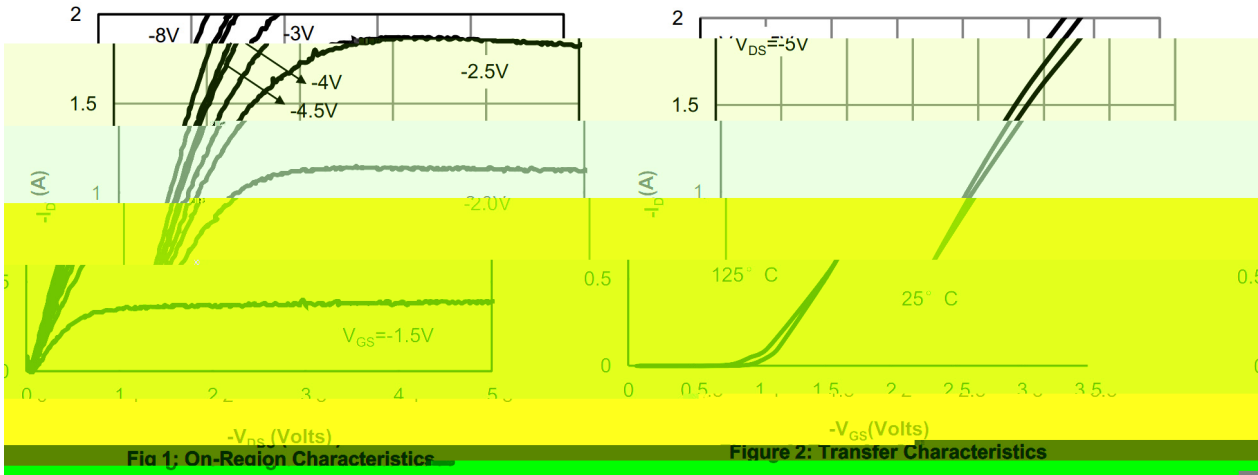
## / Absolute Maximum Ratings(Ta=25 )

Parameter	Symbol	Rating	Unit	
Drain-Source Voltage	V <sub>DSS</sub>	-20	V	
Gate-Source Voltage	V <sub>GSS</sub>	...12	V	
Drain Current – Continuous	I <sub>D</sub>	-0.7	A	
Pulsed Drain Current	I <sub>DM</sub>	-2	A	
Power Dissipation	P <sub>D</sub>	0.9	W	
Storage Temperature Range	T <sub>stg</sub>	-55 to 150		
Thermal resistance, junction - ambient	t ≤ 10s	R <sub>JA</sub>	100	/W
	Steady-State		140	

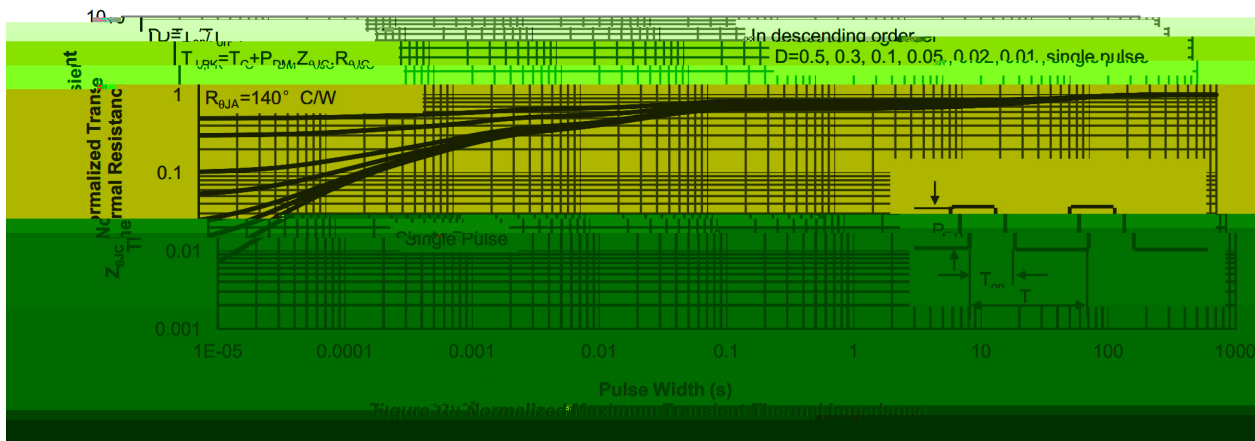
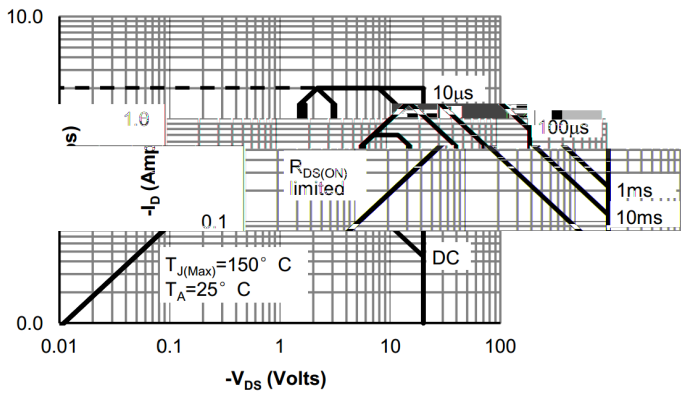
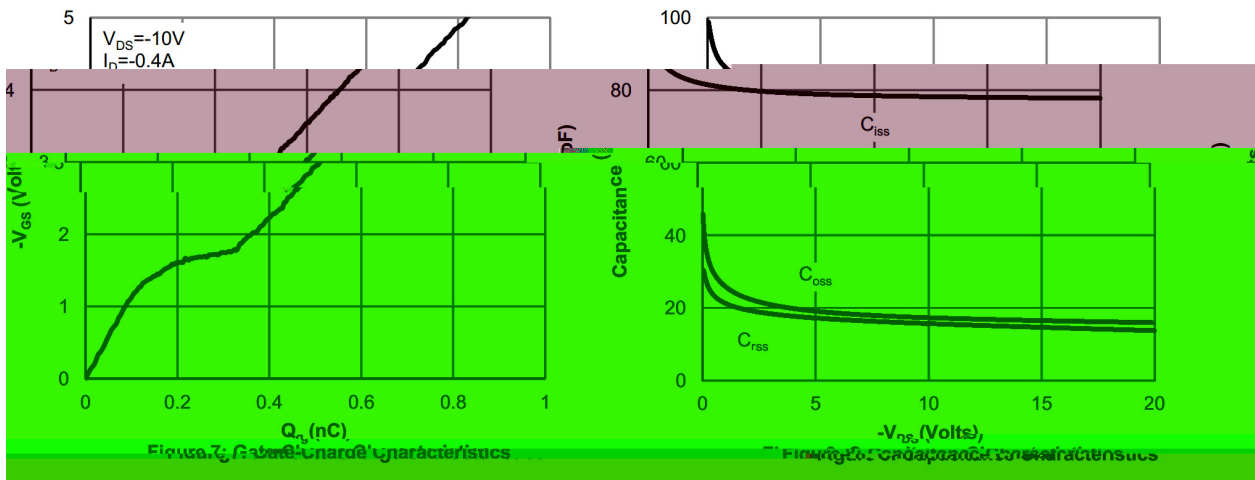
## / Electrical Characteristics(Ta=25 )

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain–Source Breakdown Voltage	V <sub>DSS</sub>	V <sub>GS</sub> =0 I <sub>D</sub> =-250uA	-20	-22		V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>GS</sub> =0 V <sub>DS</sub> =-20V			-1	μA
Gate–Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> =0V V <sub>GS</sub> =±12V			±10	μA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> I <sub>D</sub> =-250uA	-0.3	-0.6	-1.0	V
Static Drain-Source On-Resistance	R <sub>DS(on)(1)</sub>	V <sub>GS</sub> =-4.5V I <sub>D</sub> =-500mA		390	520	m
	R <sub>DS(on)(2)</sub>	V <sub>GS</sub> =-2.5V I <sub>D</sub> =-200mA		530	780	m
	R <sub>DS(on)(3)</sub>	V <sub>GS</sub> =-1.8V I <sub>D</sub> =-100mA		700	1100	m
	R <sub>DS(on)(4)</sub>	V <sub>GS</sub> =-1.5V I <sub>D</sub> =-50mA		870		m
	R <sub>DS(on)(5)</sub>	V <sub>GS</sub> =-1.2V I <sub>D</sub> =-20mA		1295		m
Drain-Source Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V I <sub>S</sub> =-250mA			-1.2	V
Gate Resistance	R <sub>g</sub>	V <sub>GS</sub> =0V V <sub>DS</sub> =0V f=1MHz		60		
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-10V V <sub>GS</sub> =0V f=1.0MHz		79		pF
Output Capacitance	C <sub>oss</sub>			15		
Reverse Transfer Capacitance	C <sub>rss</sub>			13		
Total Gate Charge	Q <sub>g</sub>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-0.4A V <sub>DS</sub> =-10V,		0.77		nC
Gate Source Charge	Q <sub>gs</sub>			0.17		
Gate Drain Charge	Q <sub>gd</sub>			0.23		
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>GS</sub> =-4.5V V <sub>DS</sub> =-10V R <sub>L</sub> =25 R <sub>GEN</sub> =3		6.2		ns
Turn-On Rise Time	t <sub>r</sub>			5.3		
Turn-Off Delay Time	t <sub>d(off)</sub>			23		
Turn-Off Fall Time	t <sub>f</sub>			8.1		

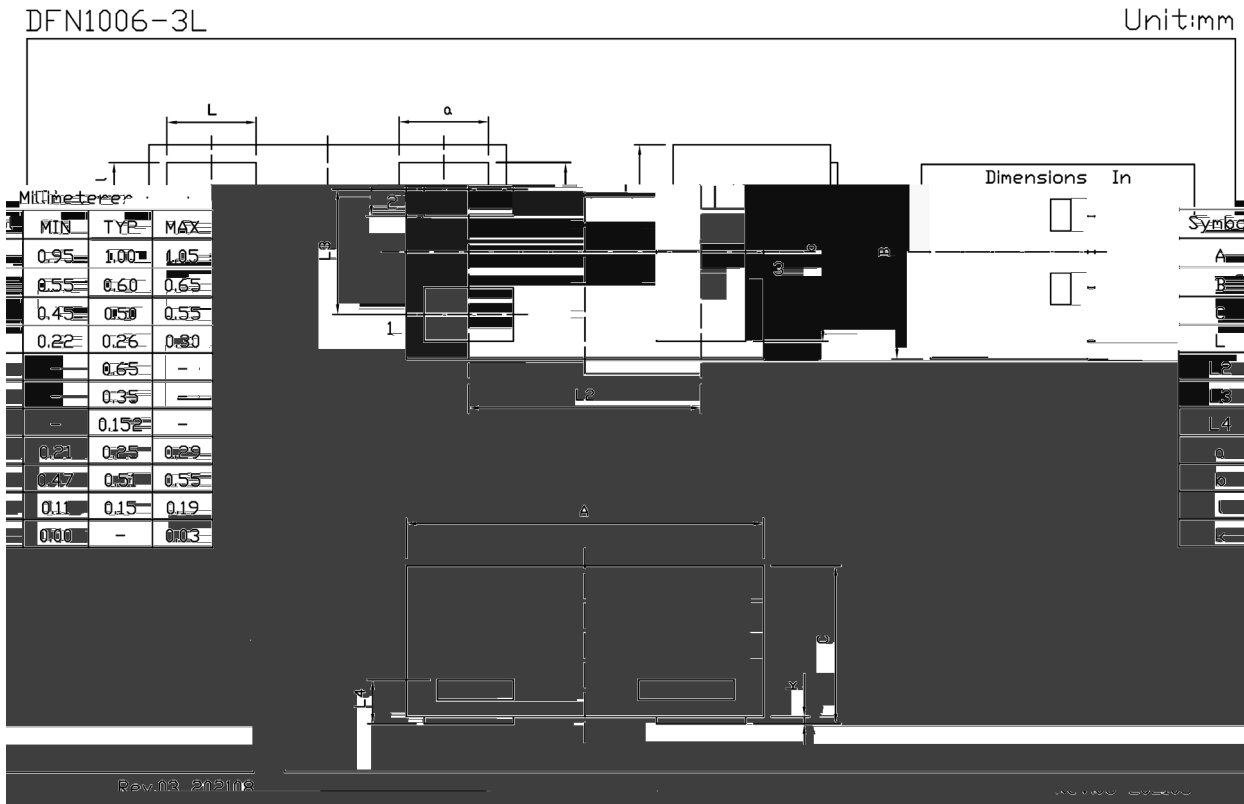
**/ Electrical Characteristic Curve**



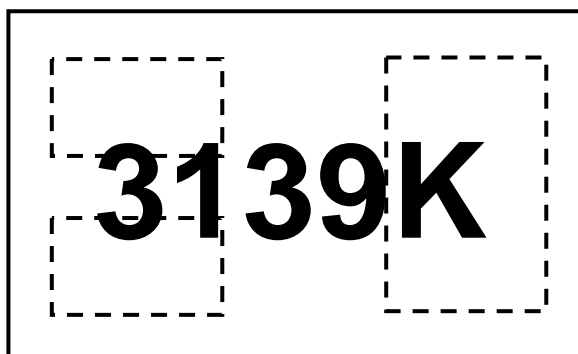
/ Electrical Characteristic Curve



/ Package Dimensions



/ Marking Instructions

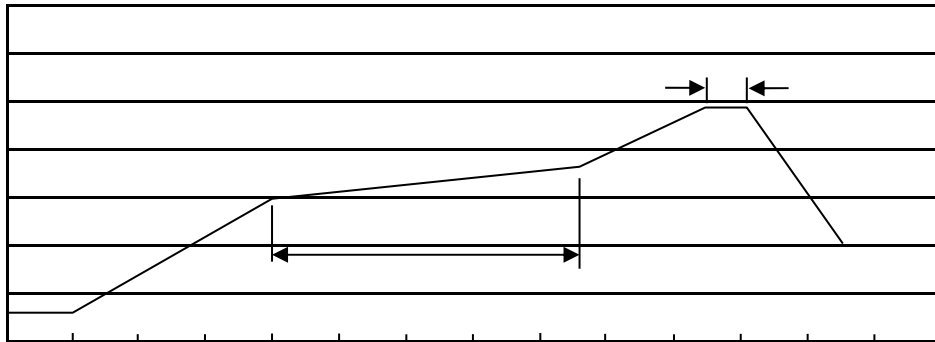


3139K

Note:

3139K: Product Type

( ) / Temperature Profile for IR Reflow Soldering(Pb-Free)



Note:

- |   |        |     |            |        |   |
|---|--------|-----|------------|--------|---|
| 1 | 150    | 180 | 60         | 90sec; | 1.Preheating:150~180 , Time:60~90sec.     |
| 2 | 245..5 |     | 5..0.5sec; |        | 2.Peak Temp.:245..5 , Duration:5..0.5sec. |
| 3 |        | 2   | 10         | /sec.  | 3. Cooling Speed: 2~10 /sec.              |

/ Resistance to Soldering Heat Test Conditions

260..5                      10..1 sec.                      Temp.:260±5                      Time:10±1 sec

/ Packaging SPEC.

/ REEL

Package Type	Units					Dimension (unit mm <sup>3</sup> )		
DFN1006-3L	10,000	10	100,000	4	400,000	7" x8	210x205x205	445x435x230

/ Notices